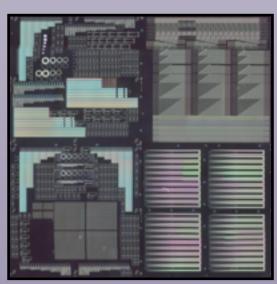
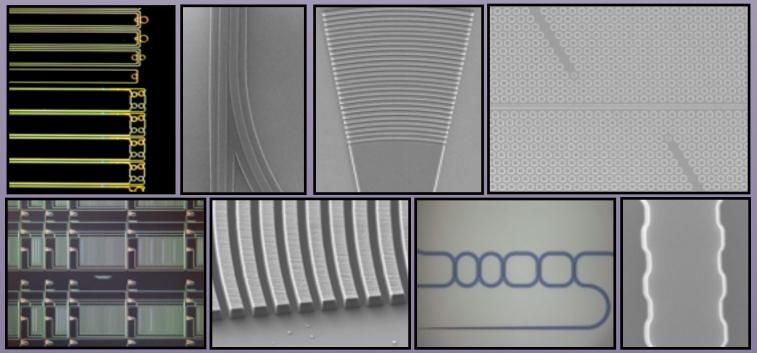
SILICON PHOTONICS

at the University of Washington's WASHINGTON NANOFABRICATION FACILITY





The Washington Nanofabrication Facility offers rapid prototyping of passive silicon photonics components featuring our high-performance JEOL JBX-6300FS electron beam lithography system and Oxford ICP etching. We offer multi-layer silicon etching, cladding, metallization, and dicing services to quickly bring your designs to reality. Our standard substrates are 25 mm square SOI chips with 220 nm Si on 3000 nm oxide, but we also work with other thicknesses and sizes. Our consistent e-beam processes offer minimum feature sizes below 100 nm and field stitching well below 10 nm, so large area, high-precision devices are reliably fabricated.



For more information, see our facility website at: http://wnf.washington.edu or contact us by email at: mff-info@engr.washington.edu



Washington Nanofabrication Facility, 215 Fluke Hall, Seattle WA 98195.

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